



Dinkarrao K. Shinde Smarak Trust's,

**DR. A. D. SHINDE**

**INSTITUTE OF TECHNOLOGY**

Approved by AICTE, Delhi, DTE Mumbai and Govt. of Maharashtra, Affiliated to MSBTE, Mumbai.

**DEPARTMENT OF ELECTRONICS AND COMPUTER  
ENGINEERING**

**NEWSLETTER**

ACADEMIC YEAR- 2025-26

SEMESTER- I

**1. FROM THE DESK OF HOD**



***Mrs. N. A. Nadagadalli***  
***Head, Department of***  
***TE Engineering***

**2. ABOUT THE DEPARTMENT**

The Department of Electronics and Computer is one of the major departments of Dr. A. D. Shinde Institute of Technology established in 2008. With the change in engineering scenario, we impart education with an appropriate skill to make our graduate competent for industry, higher education, research and entrepreneurship.

We strive to satisfy demand raised by industry by making students to face future generation technology and applications in consortium with our stakeholders like Industry and research institute of repute. Faculty members in the department modulate teaching based on their research experience and strive continuously to become effective facilitator. In the philosophy of Outcome Based Education (OBE), Curricular, Co-curricular and Extra-Curricular activities are student centric.

### 3. VISION AND MISSIONS

INSTITUTE VISION	To become an excellent technological institute committed to develop a versatile professional to satisfy the needs of industry, society, and the environment.
INSTITUTE MISSIONS	<ol style="list-style-type: none"><li>1. To provide a quality education to achieve best placements.</li><li>2. To build leadership qualities to grow professionally and as entrepreneur.</li><li>3. To develop an industry competent engineer.</li><li>4. To inculcate a sense of social and environmental responsibilities.</li></ol>
DEPARTMENT VISION	To become an excellent electronics and computer engineering department committed to provide quality education to satisfy the needs of industry and society.
DEPARTMENT MISSIONS	<ol style="list-style-type: none"><li>1. To establish center of excellence in embedded system and cloud computing.</li><li>2. To promote industry-institute and institute-institute linkages by training students in latest technologies.</li><li>3. To inculcate professional ethics to serve the society.</li></ol>

### 4. LIST OF FACULTIES

Sr	Name of the Faculty	Designation	Specialization
1.	Mrs. N.A. Nadagadalli	HOD	M.Tech (E&TC)
2.	Mr. S. S. Parit	Lecturer	M.Tech (E&TC)
3.	Mrs. J. V. Sardesai	Lecturer	BE (E&TC)
4.	Mrs. S. D. Chandure	Lecturer	BE (E&TC)
5.	Mrs. S. P. Suryavanshi	Lecturer	BE (E&TC)
6.	Mr. R. B. Shinde	Lecturer	BE (E&TC)
7.	Miss. A. U. Sutar	Lecturer	B.Tech (E&TC)

### 5. DETAILS OF MOUs

Sr	Name of the Organization	Year of MOUs	Year of Validity
1.	Rudra Electronics, Nilaji	2022	5 years
2.	Robocuit, Gadhinglaj	2022	5 years
3.	Mahalaxmi E-bikes, Sankeshwar Road, Gadhinglaj	2022	5 years
4.	Appasaheb Nalawade, Gadhinglaj Taluka, Sahakari Sakhar Karkhana Ltd., Harali	2022	5 years

## 6. DETAILS OF CO-CURRICULAR AND EXTRA-CURRICULAR ACTIVITIES ORGANIZED

Sr	Name of the Activity	Date	Resource Person	Name of the Topic
1	Guest Lecture	25/09/2025	Mr.Pote. R, A.	Code Expert Pvt.Ltd.
2	Industrial Visit	29/09/2025	Manager Hr.	ELCOM International Pvt.Ltd
3	Industrial Visit	29/09/2025	Mr.Dhanaji Ekal	Segma Sales and Services
4	Industrial Visit	29/09/2025	Mr.Kiran Patil	Indus Towers Pune
5	Students Workshop	30/09/2025 To 01/10/2025	Mr.Niranjan Desai	Design,Construction & Implement Hardware &Software Based Modern Electronics System.
6	Industrial Expert Lecture	04/10/2025	Miss. Swapnali Kharude	Analog and Digital Fault Diagnosis in Industrial Systems
7	Guest Lecture	08/10/2025	Mr. Rohit Kadam	Dynamic Power Management in Embedded Systems

## 7. LIST OF TOPPERS

Class	Name of the Student	Rank
SYTE	Shaila Sudhakar Pandit	1 <sup>st</sup>
SYTE	Anisha Anil Sutar	2 <sup>nd</sup>
SYTE	Ankita Ananda Patil	3 <sup>rd</sup>

## 8. DETAILS OF ACHIEVEMENTS OF STAFF AND STUDENTS IN CURRICULAR, CO-CURRICULAR AND EXTRA-CURRICULAR ACTIVITIES.

Sr	Name of the Staff	Details of achievement
1	Mrs. Nadagadalli N. A.	Published paper on "Artery and Vein classification in retinal images using graph-based approach"
2	Mr. S. S. Parit	Published paper on "Altera Wide band Antenna with Reduced Rader Cross Section Area"

Sr	Name of the Student	Details of achievement
1	Mr. Sumit Mahadev Jadhav Mr.Ram Mahadev Vhanji	2 <sup>nd</sup> Prize in Project Competition at Smartspark 1.0 , DADSCOE

## 9. Photo Gallery





**Gadhinglaj, Maharashtra, India**   
6928+3gj, Mandir Rd, Gadhinglaj, Maharashtra 416502, India  
Lat 16.200611° Long 74.366588°  
30/09/2025 10:46 AM GMT +05:30

 GPS Map Camera



**Talandage, Maharashtra, India**   
Plot E-4, Midc, Kagal-hatkanangale Five Star Industrial Area Kasba: Sangaon Taluka:  
Kagal Dist: Kagal 5 Star Midc Industrial Area, Talandage, Maharashtra 416236, India  
Lat 16.603751° Long 74.35412°  
29/09/2025 01:30 PM GMT +05:30

 GPS Map Camera





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